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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10073751	02/09/2002	257	758	2841	Chu

**APPLICANTS: Kar Roy Arjun; Howard David; Liu Q.Z.;

2815

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/276,277 03/15/2001

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed	<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
25 USC 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no	01CON211P
Verified and Acknowledged Examiners's initials		
TITLE : Method for fabricating a metal resistor in an IC chip and related structure		
U.S. DEPT. OF COMM. : PAT & TM - PTO-436L Rev. 12-94		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		<i>Assistant Examiner</i>	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		4	11
Primary Examiner		Print Fig.	
		11	
TERMINAL		Application Examiner	
DISCLAIMER		PREPARED FOR ISSUE	
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